

DETAILED ACTION

Allowable Subject Matter

1. The following is an examiner's statement of reasons for allowance:

None of the prior art of record is seen to disclose or suggest the limitation of claim 5 that wherein the changeover device for the gate voltage is coupled to a correction device in which, for the different gate voltage values, corresponding different correction values for additional high frequency properties of said high frequency switch (transmission or reflection) are stored which, depending on the gate voltage chosen, are used for correcting these additional high frequency properties of said high frequency switch.

For example, the closest reference found, Evers et al. (US 6,873,200), teaches an electronic high frequency switch with a field effect transistor as the switching element, whose switching condition is controlled via the gate voltage fed from a gate voltage source and is controlled by means of a control circuit between a switching on value and switching off value, characterized in that the size of the gate voltage fed from the gate voltage source is selectable between a first voltage and a second voltage by a changeover device depending on the desired linearity or switching speed, wherein neither the first voltage nor the second voltage is a ground voltage, but does not teach wherein the changeover device for the gate voltage is coupled to a correction device in which, for the different gate voltage values, corresponding different correction values for additional high frequency properties of said high frequency switch (transmission or reflection) are stored which, depending on the gate voltage chosen, are used for correcting these additional high frequency properties of said high frequency switch.

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Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to JOHN W. POOS whose telephone number is (571)270-5077. The examiner can normally be reached on M-F (alternating Fridays off), E.S.T.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lincoln Donovan can be reached on 571-272-1988. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Kenneth B Wells/
Primary Examiner, Art Unit 2816

/J. W. P./
Examiner, Art Unit 2816

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